

May, 2018  
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## **EMPLOYMENT HISTORY**

### **DataDomain (Dell/EMC) \*Principle QA Engineer \* 10/15 - Present R.T.P, NC.**

Providing Technical Leadership and Guidance for team and project to improve test plan/case development and execution both automated and manual. PERL, RUBY, PYTHON, system programming, SQL, ORACLE, VMWARE. Customer problem recreation for fix verification. Using Agile/Scrum development processes and tools to provide continuous integration feature delivery. Continued development and maintenance automatic health and problem detection tool. Performance benchmark testing to provide performance base line and regression identification. Continuously drive high Quality into product releases with Quality Engineering techniques and principles. Cloud based Test Environment Setup and Execution AWS, AZURE, EMC/ECS2.

### **DataDomain (EMC) \*Senior QA Engineer \* 4/11 - 10/15 R.T.P, NC.**

In the DDBOOST (DataDomain Bandwidth Optimized Open Storage Technology) component area. Designing, Writing, Executing Test Cases Manually and Automated using Ruby (now transitioning to using Python), Perl and Manual test case execution for Enterprise Backup Applications: Oracle 10g 11g RMAN, Symantec's NBU and BE products and EMC Networker. Fiber Channel test environment setup and testing on NAS/SAN. Develop Nightly Automation Build tests. Test Lead on Maintenance and patch releases. Writing Test plans, Test Specifications, and test case, project planning sizing, etc.

### **Tivoli (IBM) \*Staff Engineer (team lead)\* 6/00 - 4/11 R.T.P, NC.**

Security compliance and vulnerability testing and certification. Test case writing, preparing and executing manual and automated testing on next release. J2EE application. Test for ITIM enterprise, VMM, Access Manager. Designed, Developed and trained testers on the end to end automated test application adapted for FVT-SVT test cycle, still in use by test teams in India to date for OSDB certification of Tivoli Privacy Manager. Latest skills developed in Middleware WAS 4-7, DB2 6-9, Oracle9-10, IDS, Netscape Directory, automated verification using API, JMETTER, JUNIT.

Team Lead throughout product development cycle in Software Verification. Test process, tool, and automated testing development for Joint Tivoli Cisco OEM VPN monitor. Real and simulated environments design and setup for software verification of VPN monitor application. JAVA Script, Applets, Servlets, JSP. IPSec, L2 tunneling, SNMP, IP, Router protocols.

**TSIEM - Tivoli Security Event Manage** - Security Event identification via managed end point log collection and analysis.

**TKLM - Tivoli Key Life Cycle Manager** - Vulnerability Test Lead - Keys, Key Groups and Certificates used to en/decrypt data on Disc and Tape.

**ITIM - IBM Tivoli Identity Manager** - Technical Lead - Managing Access and User accounts using WF and Policy Features.

**ITPM - IBM Tivoli Privacy Manager** - Automation Test Lead - Managing Access compliance for SBOX HIPPA. VPM Monitor - Cisco OEM partnership - Test Team Lead - Application to monitor VPN traffic on Cisco and IBM Routers, concentrators.  
JAVA Script, Applets, Servlets, JSP. IPsec, L2 tunneling, SNMP, IP, Router protocols.

**I.B.M (contractor) \*Network Engineer\* 4/99 - 6/00**

**R.T.P, NC.**

Internet Security, Firewall Software Verification. TCP/IP, AIX, NT.  
Test Case development and execution. All Internet protocols. WAN topology design and configuration.

**Ericsson \*Staff Engineer\* 4/97 - 4/99**

**R.T.P.,NC.**

Cellular digital phone software design, development, and verification. Complete product life cycle planning, scheduling, and development. TDMA IS-136/130/135 software specification based development.  
Cellular Base Station, Terminals (phone), Switch, and Network. Test coding for simulations over Air Interface and test boxes. Development on all layers from hardware up to high level protocol.  
Solaris, SCO and Linux Unix.

**I.B.M (contractor) \*Network Test\* 3/96 - 4/97**

**R.T.P.,NC.**

Token Ring and Ethernet, Network Test Environment Modeling for card adapter's quality assurance. TCP/IP, Warp, Dos, Windows[all], Novell, Lantastic, VM, Routers, Gateways, Novel. Lan Server, Bridges, AIX.

**Logical Design Group \* Software Engineer \* 7/94 - 3/96**

**Raleigh, NC.**

Engineering Embedded Intel for VMEbus and Industrial systems. Automated test code design and development for Hardware Platforms using DOS/Windows on ISA /EISA, PCI, VLB, VME. System BIOS development. GUI design for driver interfaces. Drivers, Network Engineering, Administration. Client/Server. Windows[all], DOS, Unix, Borland C++, MSVC++, MASM, TASM, System BIOS, RT, QNX, OS/9, OS/2, NETBIOS, TCP/IP, Internet Administrator.

**Logical Design Group \* Technician/Programmer \* 7/92 - 6/94**

**Raleigh, NC.**

Manufacturing Embedded Intel, Motorola, DEC micros for Industrial computer (VME) and video editing systems.  
Test and test software and Test fixturing. Test procedures. Environmental test chamber Interface design, code, and test.  
System to component Troubleshooting. Windows, Labviews, Norton, Microsoft Quick C, Assembler, DOS.

**Winchester Electronics \* Mainframe User Support \* 1/90 - 1/91  
Watertown, CT.**

NCR Mainframe User Support, Terminal, Micro's, and Printer repair.  
Terminal, Printer, Modem, etc, installation.

**Micrognosis, Inc. \* Senior Engineering Technician \* 3/85 - 3/89  
Danbury, CT.**

Multi Workstation, Resource Distributed systems for trading floor market.  
Design and perform system tests for feature, function, and performance on Embedded Intel and Thick Ethernet. Network Traffic and Collision analysis using Network Analyzers. Spectrum Analyzers, Datascopes, Oscopes. UL, VDE, RFI, EMI conformity and qualification.  
Waveform/Frequency counters, generators, Facilities power planning. DOS, Pick, assembler, CP/M, Lotus.  
EDUCATION

**Naugatuck Community College (now UCONN) 8/90 - 5/92**

A.S. Data Processing (3.48 GPA - Honors)  
CMS/MVS, Pascal, C, Assembler, CASE, COBOL, PL/1, DOS

**Porter and Chester Institute 1/83 - 1/85**

A.S Computer Electronics Technology (3.2 GPA)  
Z80, 80x8X, 68000, UARTs, assembler, CP/M, AC/DC, Motors, Encoders, resolvers, controllers.